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Applicants: Donald B. Eidson, et al.  
Serial No.: 09/602,690  
For: Rate N/N Systematic, Recursive Convolutional Encoder And  
Corresponding Decoder  
Filed: June 23, 2000

**TRANSMITTAL OF SUPPLEMENTAL INFORMATION DISCLOSURE  
STATEMENT FORM 1449**

Attorney(s): Robert C. Laurensen  
Docket No.: 01827.0037.00US00  
Date of Deposit: March 8, 2001, via First Class Mail  
Enclosure(s): Supplemental Information Disclosure Statement; PTO-1449;  
References (4); Return Postcard

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Donald B. Eidson, et al.

Application No.: 09/602,690

Filed: June 23, 2000

For: **RATE N/N SYSTEMATIC, RECURSIVE  
CONVOLUTIONAL ENCODER AND  
CORRESPONDING DECODER**

Art Unit: 2734

Examiner: Unassigned

1c903 U.S. PTO  
09/887877  
06/21/01

**SUPPLEMENTAL  
INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Applicant hereby submits, without admission of prior art effect thereof, the documents listed on the accompanying Form PTO-1449 pursuant to the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Applicant has listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

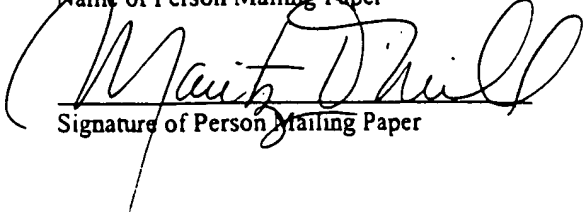
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**CERTIFICATE OF MAILING**  
(37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached hereto) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

March 8, 2001  
Date of Deposit

Maritza O'Neill  
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Signature of Person Mailing Paper

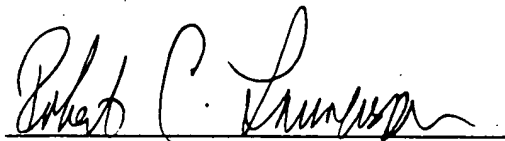
This Information Disclosure Statement is being filed within three months of the U.S. filing date or before the mailing date of a first Office Action on the merits, therefore no statement under 37 C.F.R. § 1.97(e) or fee is required.

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and to indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 08-3038.

Respectfully submitted,

Date: March 7, 2001



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**LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S  
INFORMATION DISCLOSURE STATEMENT**

(Use several sheets if necessary)

ATTY. DC NO.  
01827.0037.00US00

SERIAL NO.  
09/602,690

APPLICANT:  
Donald B. Eidson, et al.

FILING DATE:  
June 23, 2000

GROUP:  
2734

09/602,690 PTO  
06/21/01

**U.S. PATENT DOCUMENTS**

| EXAMINER<br>INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB<br>CLASS | FILING<br>DATE |
|---------------------|-----------------|------|------|-------|--------------|----------------|
|                     |                 |      | None |       |              |                |
|                     |                 |      |      |       |              |                |

**FOREIGN PATENT DOCUMENTS**

| EXAMINER<br>INITIAL | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUB<br>CLASS | TRANSLATION<br>YES NO |
|---------------------|-----------------|------|---------|-------|--------------|-----------------------|
|                     |                 |      | None    |       |              |                       |
|                     |                 |      |         |       |              |                       |

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)**

|    |  |
|----|--|
| AA | Benedetto, S. and Montorsi, G., <i>Iterative decoding of serially concatenated convolutional codes</i> ; <b>Electronics Letters</b> , Vol. 32, No. 13, June 1996, pages 1186-1188.   |
| AB | Benedetto, S. and Montorsi, G., <i>Serial concatenation of block and convolutional codes</i> ; <b>Electronics Letters</b> , Vol. 32, No. 10, May 1996, pages 887-888.  |
| AC | Robertson, P., et al., <i>A Comparison of Optimal and Sub-Optimal MAP Decoding Algorithms Operating in the Log Domain</i> ; <b>IEEE</b> , 1995, pages 1009-1013.   |
| AD | Viterbi, Andrew J., <i>An Intuitive Justification and a Simplified Implementation of the MAP Decoder for Convolutional Codes</i> ; <b>IEEE Journal on Selected Areas in Communications</b> , Vol. 16, No. 2, February 1998, pages 260-264. |

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant